

Attorney's Docket No.: 07977-226002 / US3548D1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hisashi Ohtani

Serial No.: 09/596,755

Art Unit : 1765

Filed

Examiner: Robert Kunemund

Title

: June 15, 2000

: MANUFACTURING METHOD OF SEMICONDUCTOR AND

MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

ASSOCIATE POWER OF ATTORNEY

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Respectfully/submitted,

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